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GROUP 2500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

Title:

BIPOLAR TRANSISTORS WITH LOW-RESISTANCE EMITTER CONTACTS

Docket No.:

303.466US1

Filed: Examiner:

April 29, 1998

Serial No.: 09/069,668

Due Date: N/A

Group Art Unit: 2814

Assistant Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

An Information Disclosure Statement (1 pgs.), Form 1449 (4 pgs.), and copies of 55 cited references. X

X A Communication Concerning Copending Applications (1 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

Arty: Eduardo E. Drake

Reg. No. 40,594

(GENERAL)

S/N 09/069,668

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

BIPOLAR TRANSISTOR

Serial No.: Filed:

Title:

09/069,668

April 29, 1998

JUL 1 7 1998

Examiner:

RESISTANCE EMITTER CONTACTS

Group Art Unit: 2814

Docket: 303.466US1

COMMUNICATION CONCERNING COPENDING APPLICATIONS RECEIVED

Assistant Commissioner for Patents

Washington, D.C. 20231

JUL 2 2 1998

PATENT

GROUP 2500

Applicants would like to bring to the Examiner's attention the following list of additional co-pendingapplications for the above-identified patent application.

Serial No.	Filing Date	Title
09/031,637	February 27, 1998	Highly Conductive Composite Polysilicon Gate for CMOS Integrated Circuits
09/030,430	February 25, 1998	Methods and Structures for Metal Interconnections in Integrated Circuits
09/030,113	February 25, 1998	Methods and Structures for Silver Interconnections in Integrated Circuits
09/030,188	February 25, 1998	Methods and Structures for Gold Interconnections in Integrated Circuits

Respectfully submitted,

KIE Y. AHN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBER'S, WOESSNER & KLUTH, P.A.

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Minneapolis, MN \$5402

(612) 349-9593

Eduardo E. Drak

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231 on July 2, 1998.

Signature

<u>S/N 09/069,668</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kie Y. Ahn et al.

Serial No.:

09/069,668

Filed:

April 29, 1998

Title:

BIPOLAR TRANSISCORS W

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Examiner:

Group Art Unit: 2814

Docket: 303.466US1

LOW-RESISTANCE EMITTER

CONTACTS

INFORMATION DISCLOSURE STATEMENT

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GROUP 2500

Assistant Commissioner for Patents Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialled by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KIE Y. AHN ET AL.

By their Representatives,

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Date

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